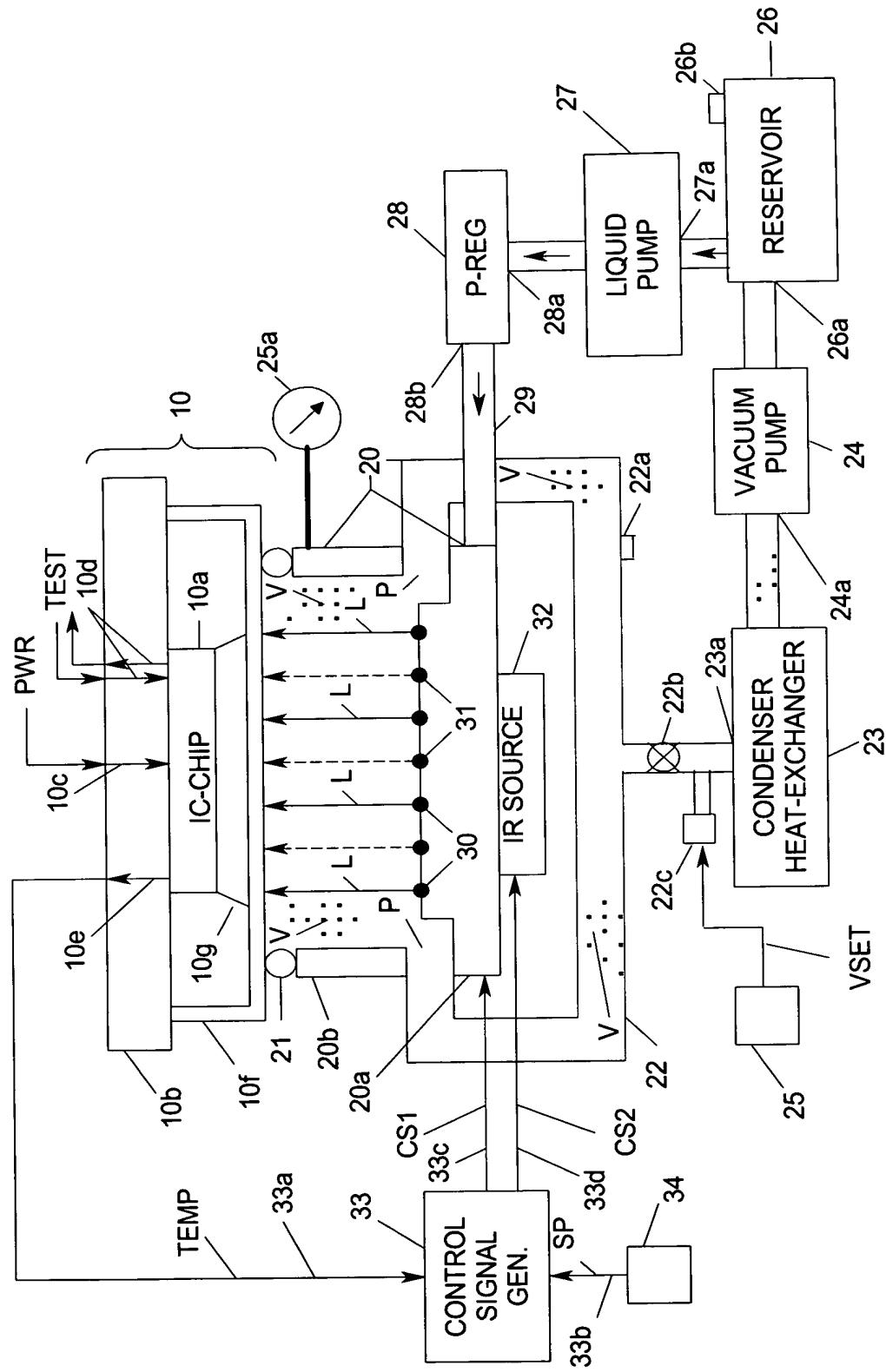


FIG. 1



**FIG. 2**

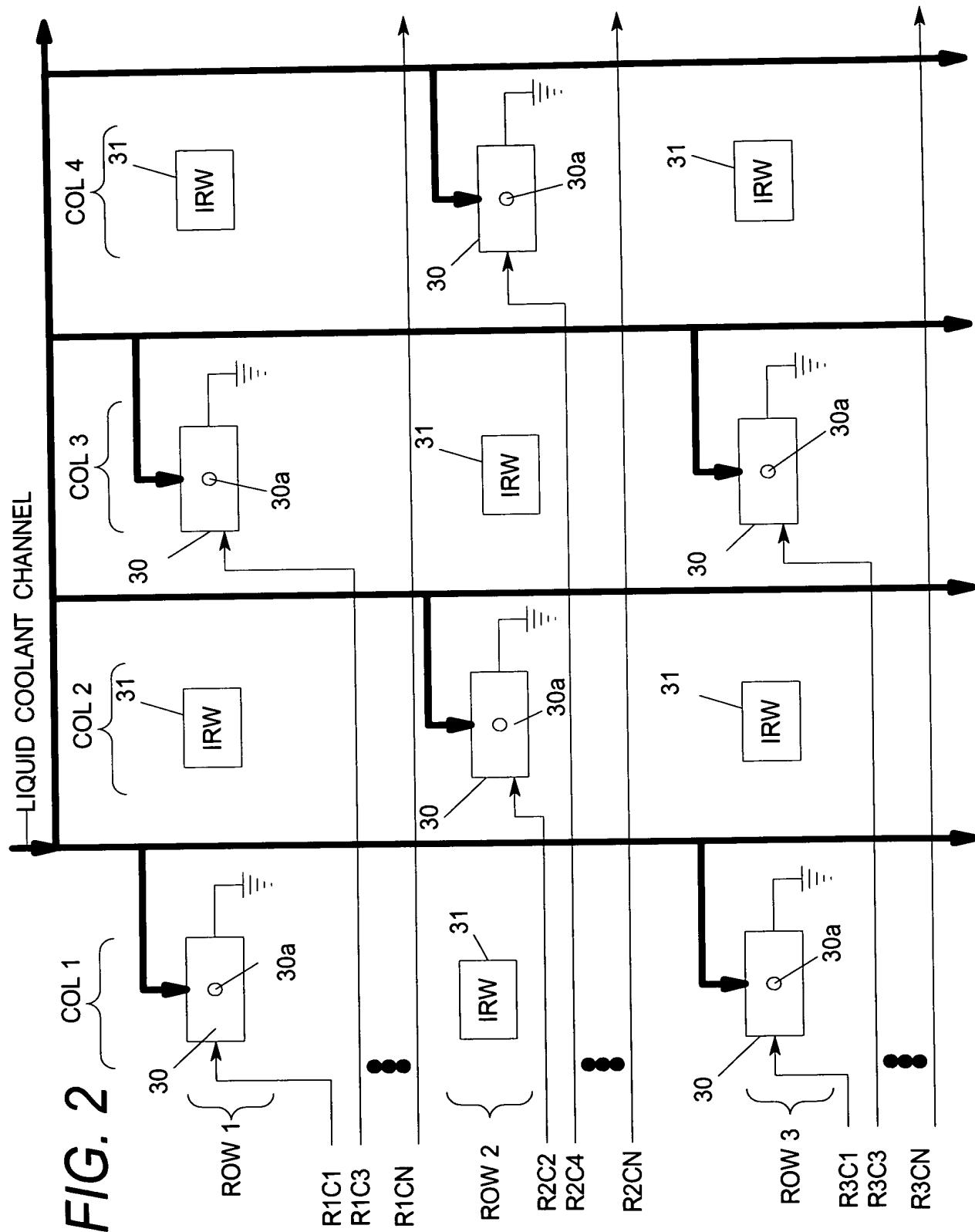
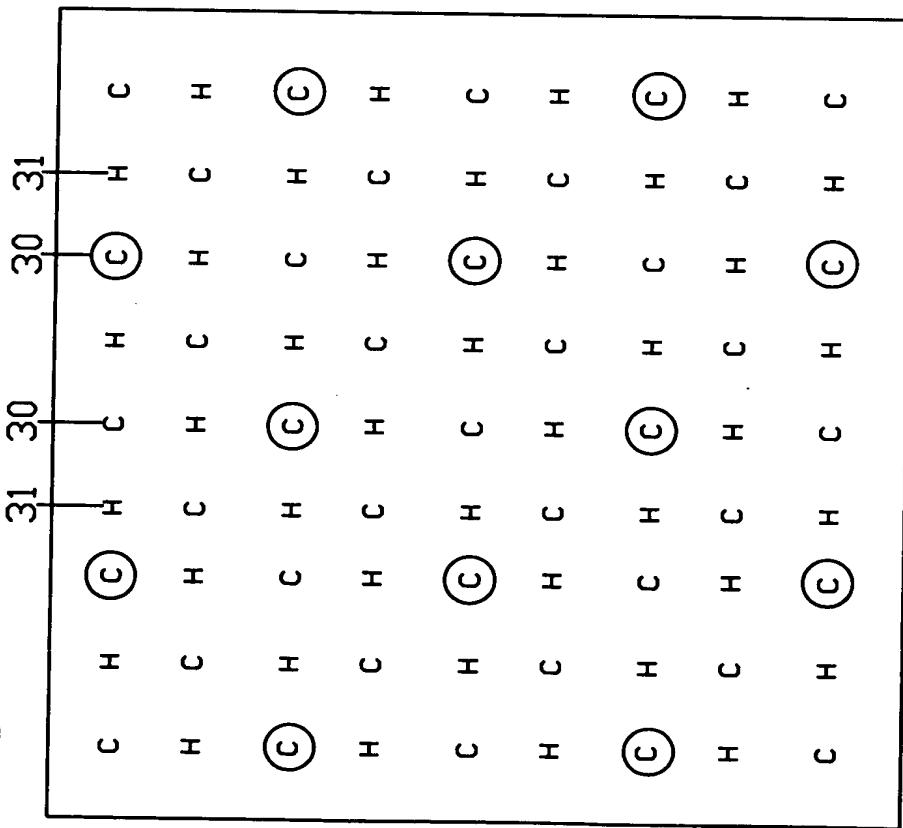
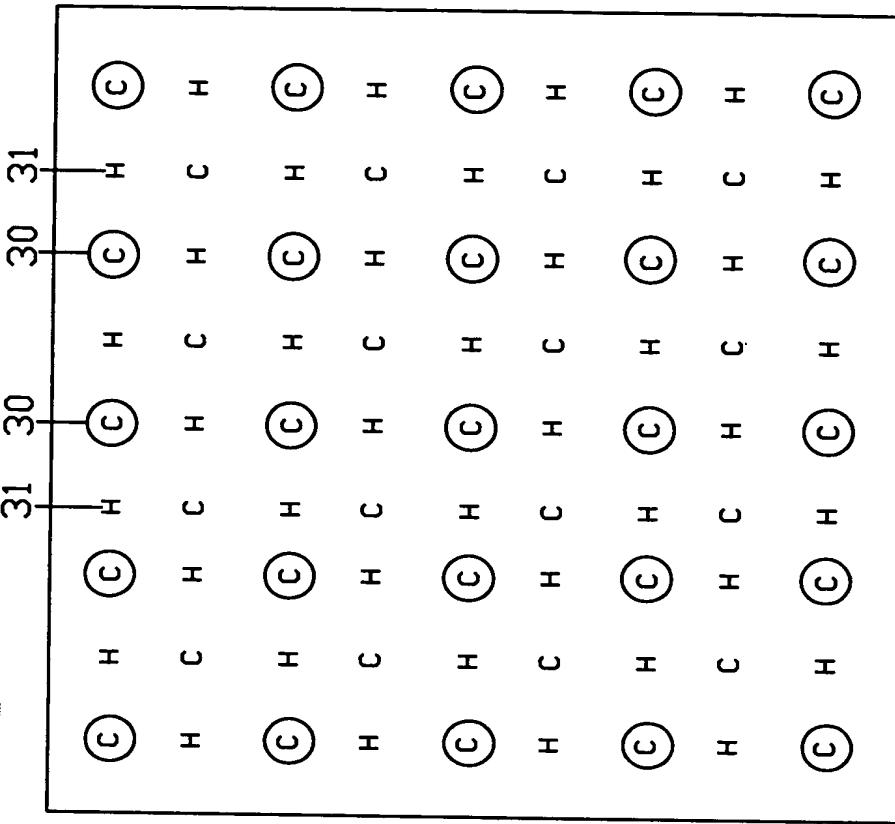


Fig 3A



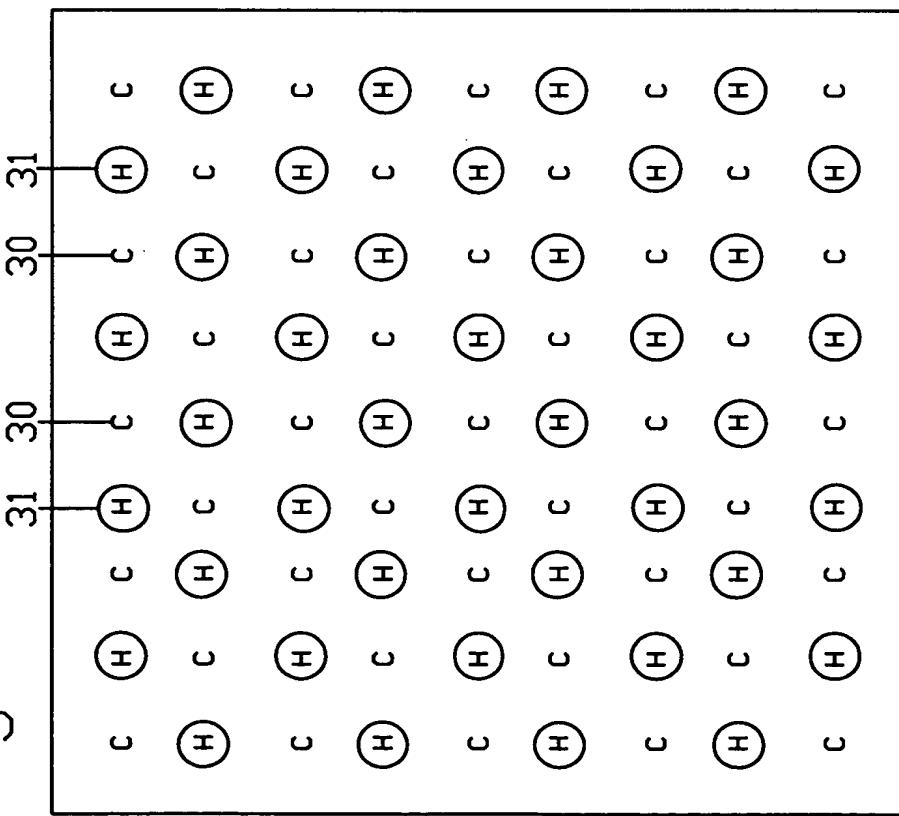
Chip Power = 100W  
Chip Temp =  $T_C$

Fig 3B



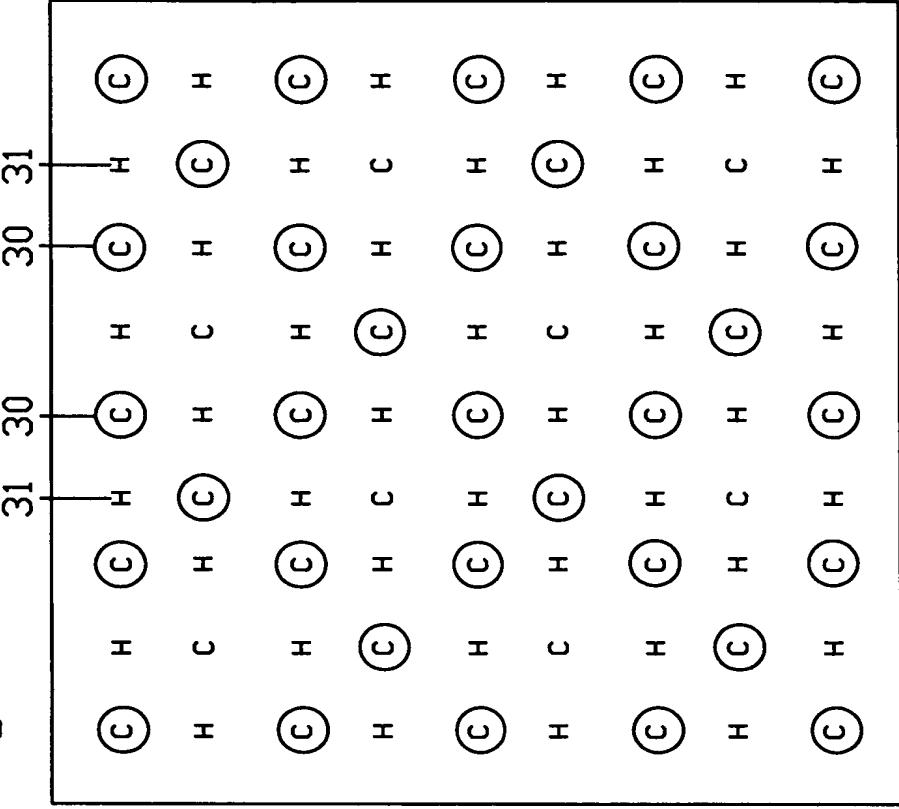
Chip Power = 200W  
Chip Temp stays at  $T_C$

Fig 3C



Chip Power = 0W  
Chip Temp stays at  $T_C$

Fig 3D



Chip Power = 300W  
Chip Temp stays at  $T_C$

# FIG. 4

$$eq. 1 \sim 1 \text{ drop} = 10 \text{ picoliter} = 10 * 10^{12} \text{ lit} \frac{10^3 \text{ gr}}{\text{lit}} = 10^8 \text{ gr}$$

$$eq. 2 \sim \Delta Q/drop = \left[ (\Delta T)(c_p) + 2260 \frac{J}{gr} \right] \frac{10^{-8} \text{ gr}}{\text{drop}} \approx 20 \frac{\mu J}{\text{drop}}$$

$$eq. 3 \sim 400 \frac{J}{\text{sec}} = 20 \frac{\mu J}{\text{drop}} \begin{bmatrix} \# \text{of} \\ \text{nozzles} \end{bmatrix} \begin{bmatrix} \text{control} \\ \text{signal freq} \end{bmatrix}$$

$$eq. 4 \sim \text{if freq} = 10^4 \text{ cycles/sec, then} \begin{bmatrix} \# \text{of} \\ \text{nozzles} \end{bmatrix} = 2000$$

eq. 5 ~ nozzle array = (45) x (45) nozzles on 1 square inch

$$eq. 6 \sim \text{nozzle spacing} = \frac{2.54 \text{ cm}}{45 \text{ nozzles}} = \frac{560 \mu m}{\text{nozzle}}$$

$$eq. 7 \sim \text{area per nozzle} = 50 \mu m \times 100 \mu m \\ \text{area per IR-window} = 20 \mu m \times 20 \mu m$$

FIG. 5A

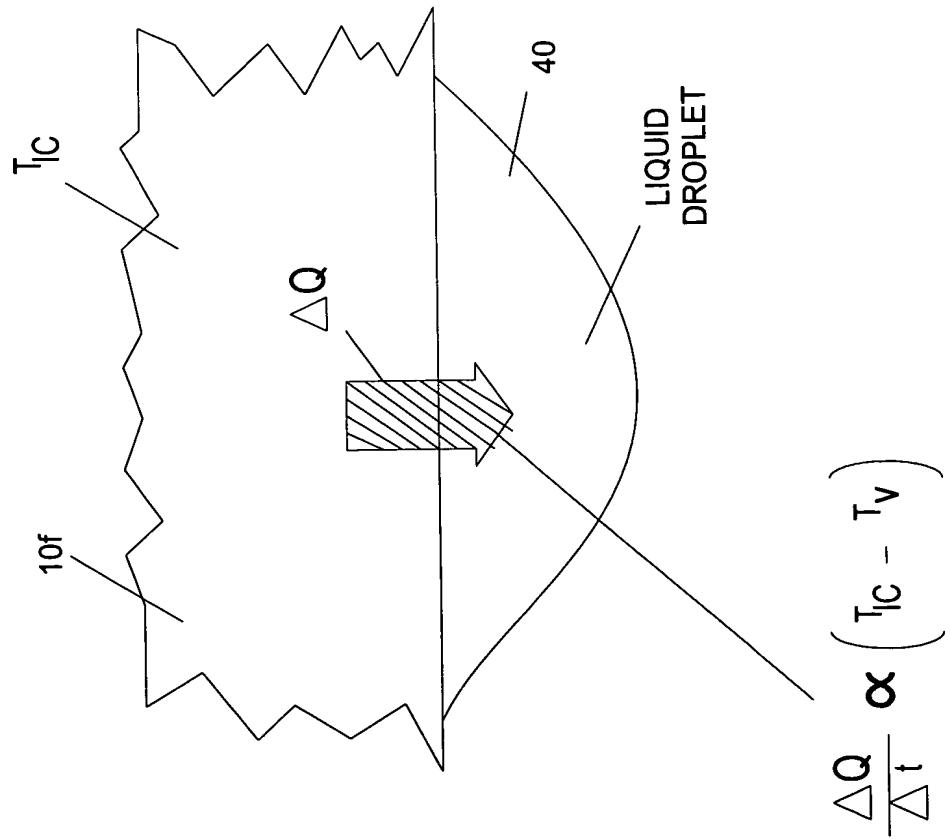
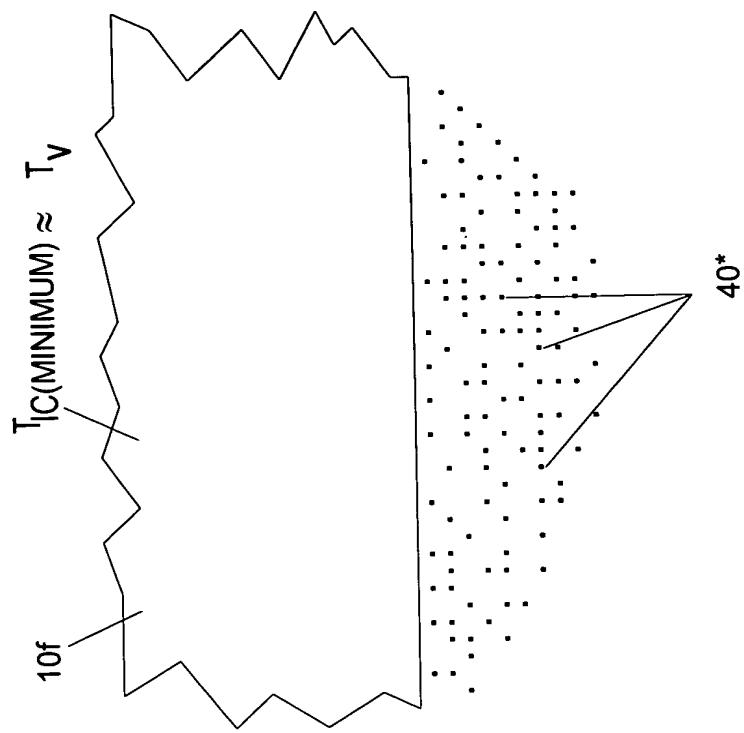
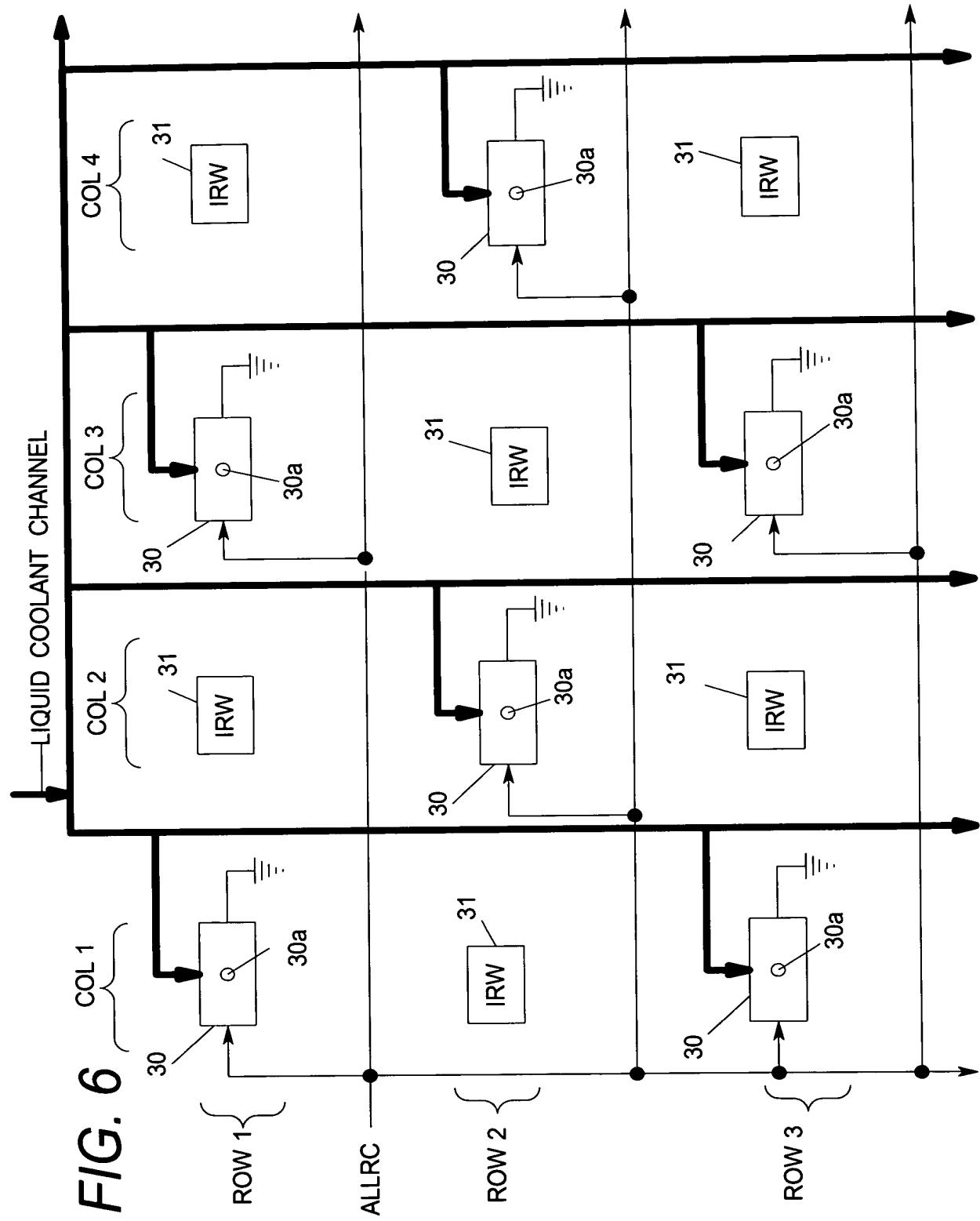


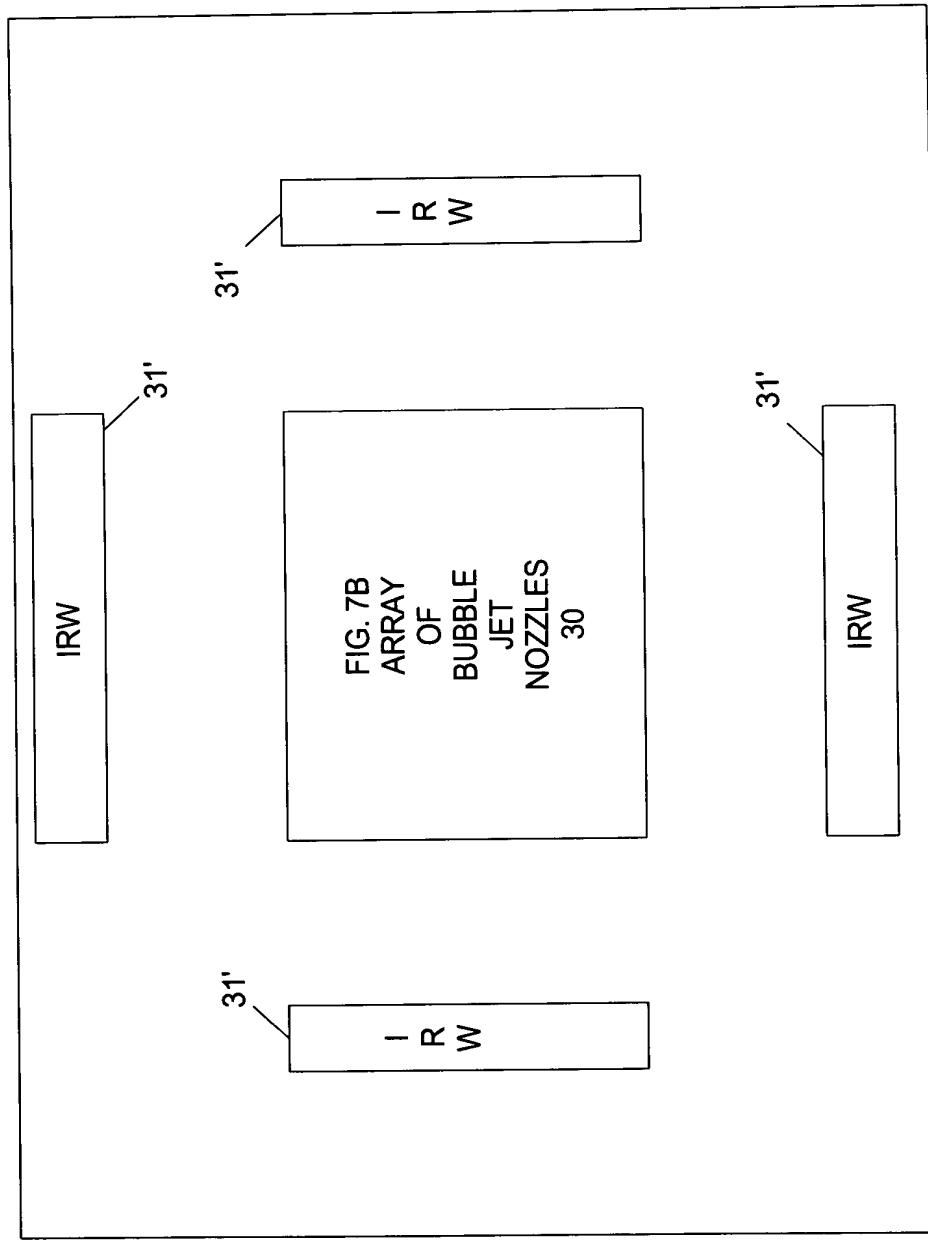
FIG. 5B



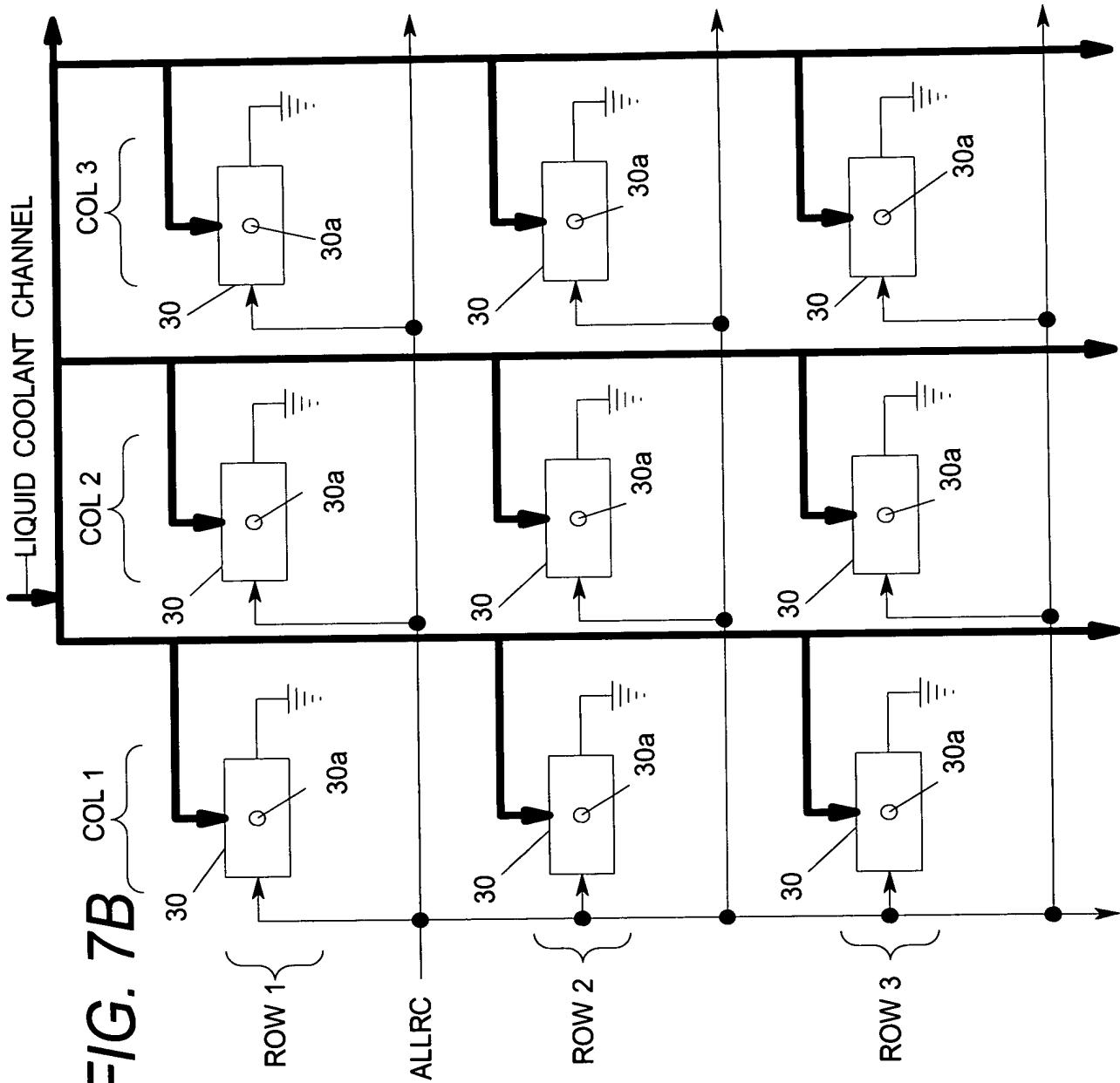
**FIG. 6**



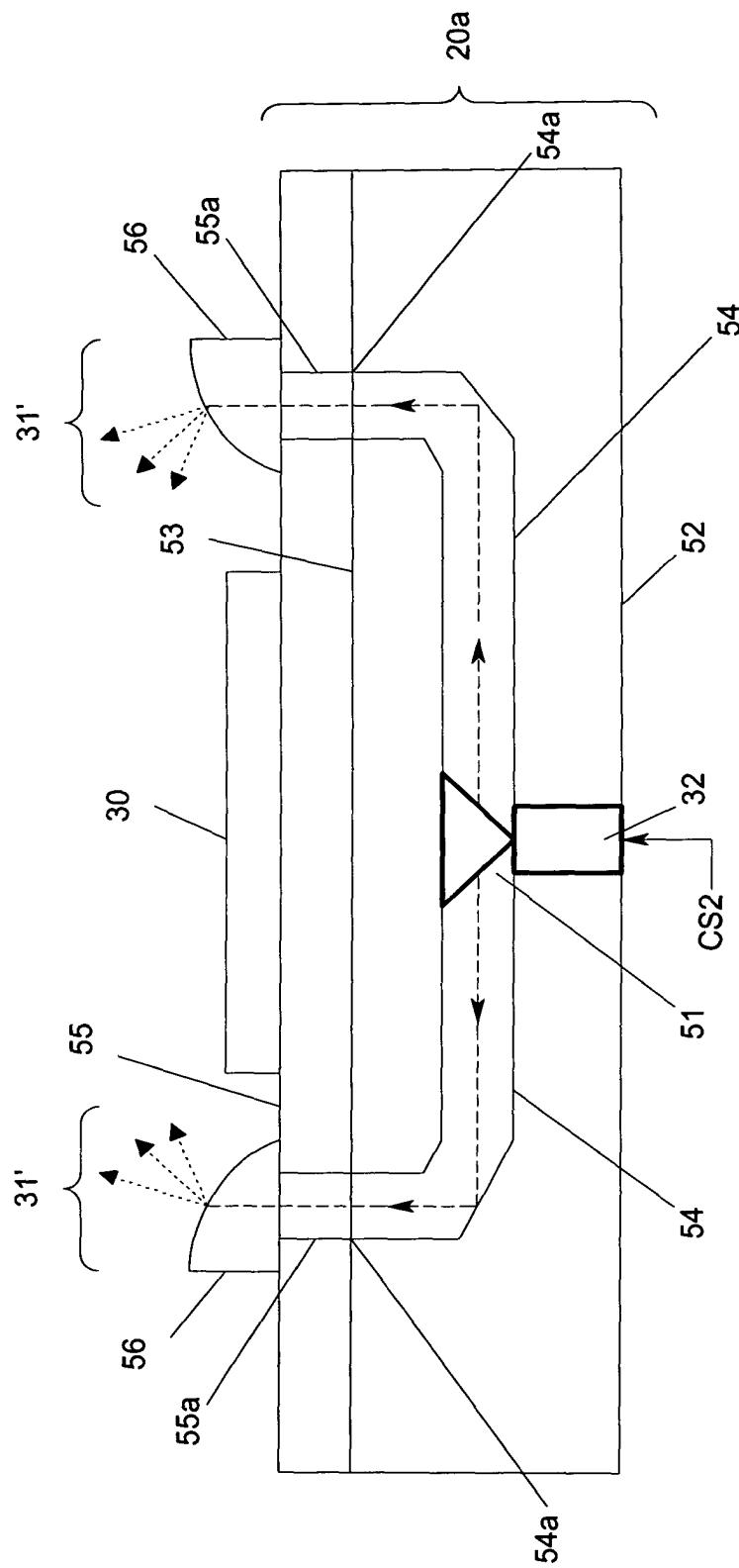
*FIG. 7A*



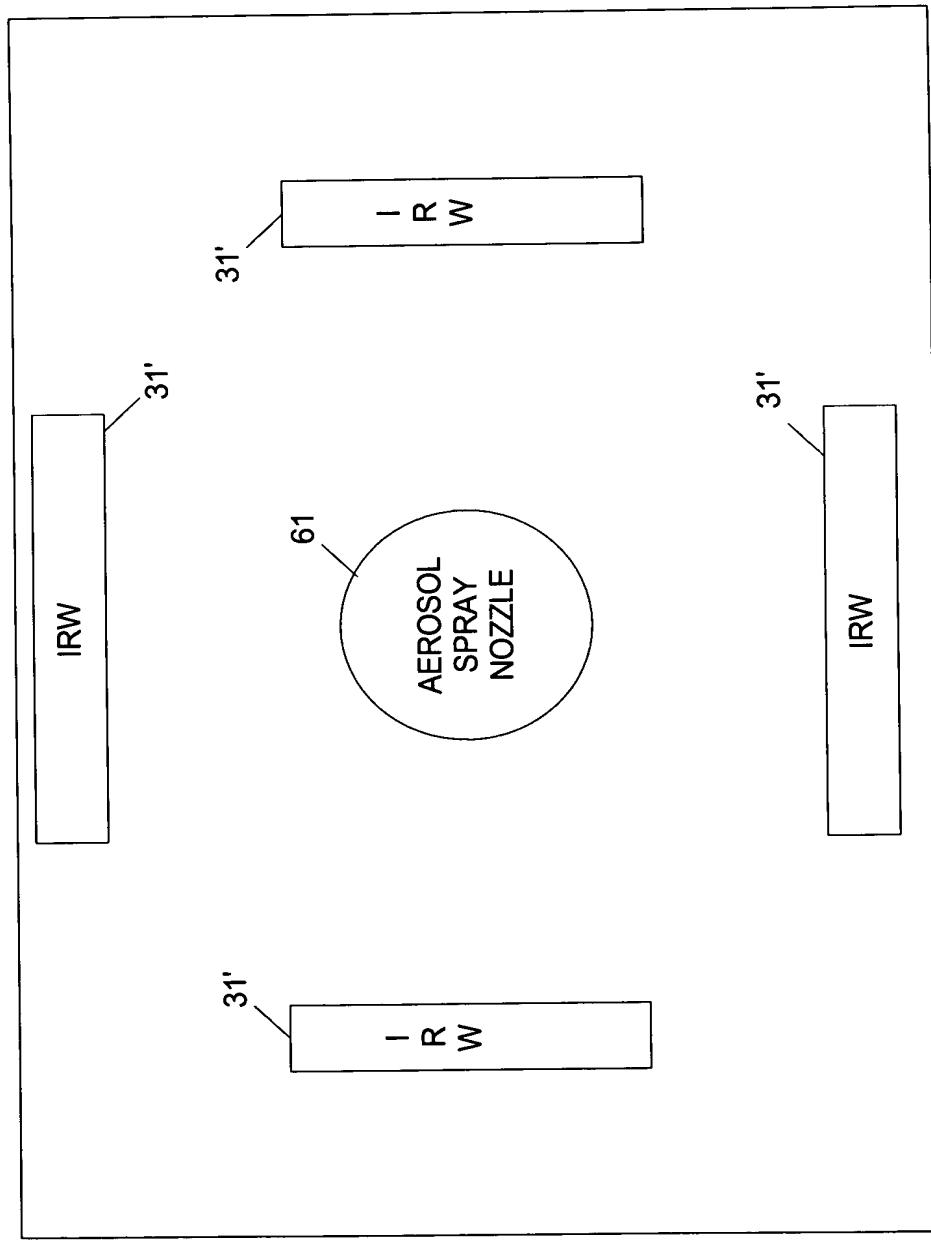
**FIG. 7B**



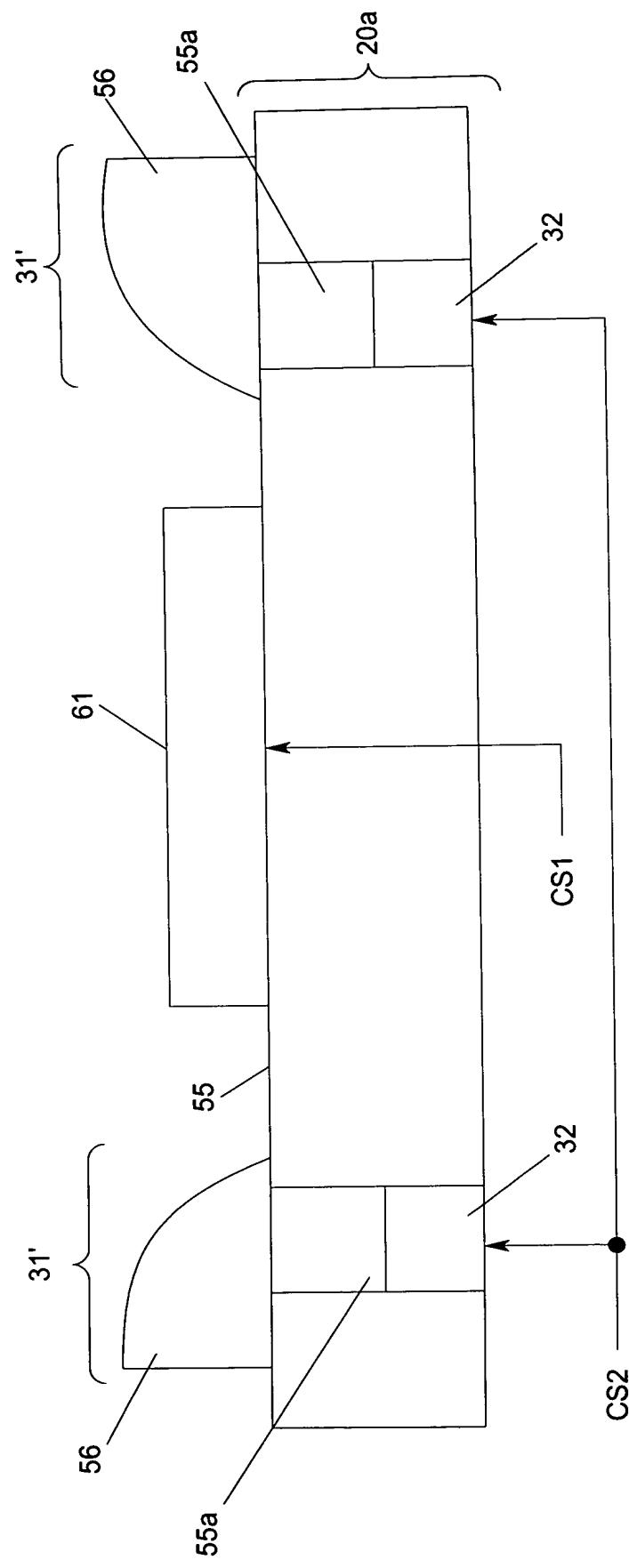
*FIG. 7C*



*FIG. 8A*



*FIG. 8B*



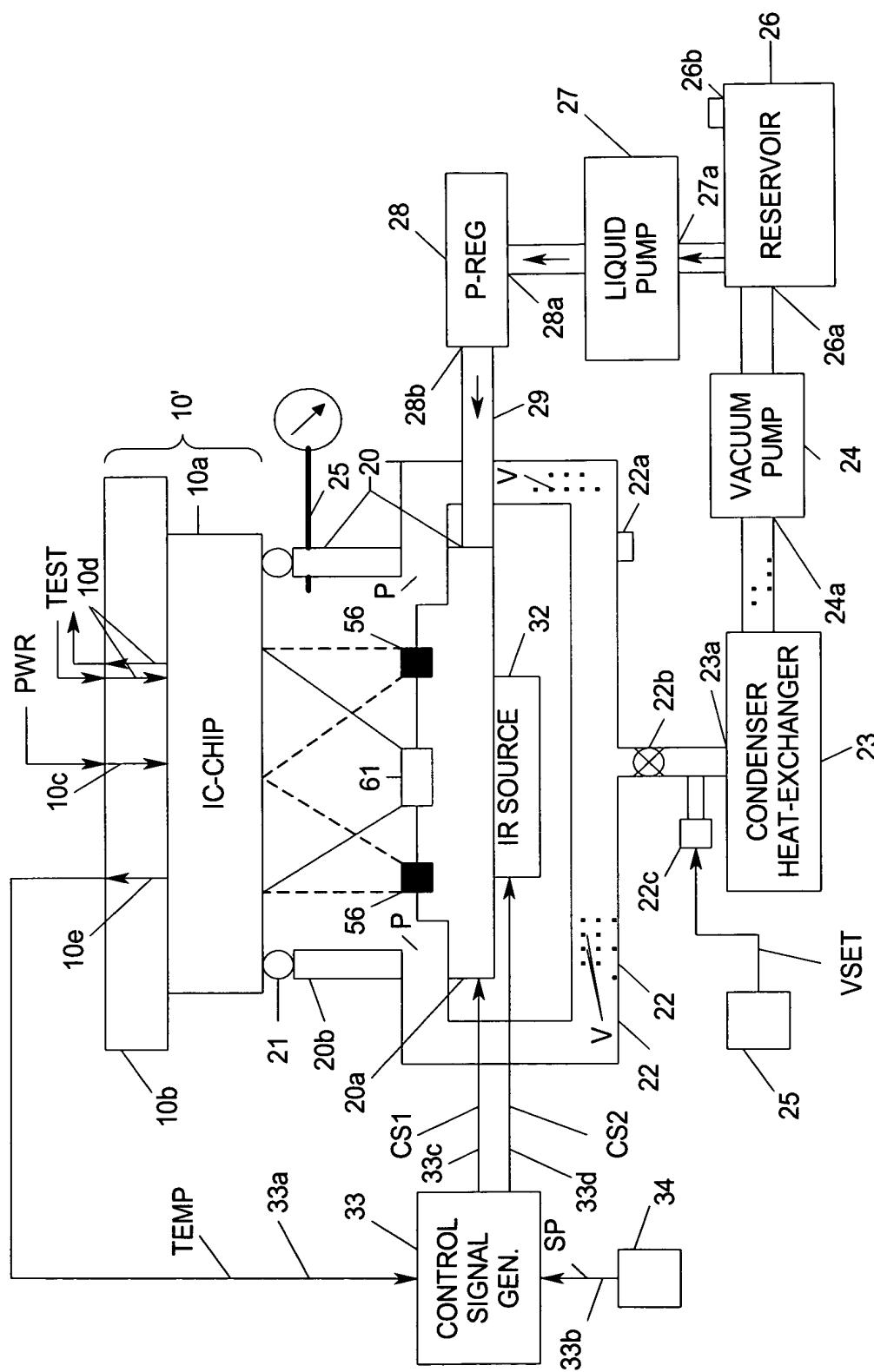


FIG. 9